

1. Design of a switch for network on chip applications

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[AbstractPlus](#)[| Full Text:](#)[PDF\(384](#)[KB\) IEEE](#)[Xplore](#)[Rights and](#)[Permissions](#)**2. High-speed table lookup engine for IPv6 longest prefix match**

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[AbstractPlus](#) | Full Text: [PDF\(772 KB\)](#) IEEE Xplore[Rights and Permissions](#)**4. New matrix formulation for two-dimensional DCT/IDCT computation and its distributed-memory VLSI implementation**

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